

## CLAIMS

What is claimed is:

- 1           1.     An interface definition comprising:  
2                 an electrical signal set definition;  
3                 a mechanical and electrical definition for a memory card;  
4                 an associated memory card signal and mechanical specification for a memory  
5     card; and  
6                 a mechanical definition for cables compatible with an interface socket.
  
- 1           2.     The interface definition of claim 1 wherein the electrical signal set definition  
2     comprises eight DPUSB signals, one reserved signal, a write protect signal and a card detect  
3     signal.
  
- 1           3.     The interface definition of claim 2 wherein pin assignments are mapped to  
2     digital pins using DPUSB signals.
  
- 1           4.     An interface socket comprising:  
2                 a signal set that defines a peripheral port, a host port, a write-protect signal; and  
3                 a form factor that is equivalent to a secure digital card.
  
- 1           5.     The interface socket of claim 4 wherein the electrical signal set definition  
2     comprises eight DPUSB signals, one reserved signal, a write protect signal and a card detect

**3** signal.

**1** 6. The interface socket of claim 5 wherein pin assignments are mapped to digital  
**2** pins using DPUSB signals.

**1** 7. The interface socket of claim 5 wherein the socket can be interfaced to both a  
**2** USB host device and a USB peripheral device.